

Mechanism of grain growth in phosphorus-doped polycrystalline silicon thin films during annealing

F Jesu Rethinam*, S Kalainathan** and R Sivarama Krishnan*

*Department of Physics, Sacred-Heart College, Tirupattur-635 601,
Tamil Nadu, India

**Department of Physics, Vellore Engineering College, Vellore-632 014,
Tamil Nadu, India

Received 4 June 1998, accepted 4 September 1998

Abstract : In the present investigation, a kinetic model has been proposed based on the thermodynamical concepts to determine the surface area of the grain in heavily phosphorus doped polysilicon thin film for various dopant concentration, annealing time and temperature of annealing. The numerical analysis for our theoretical predictions has been made using computer simulation technique. The simulated values are compared with available experimental reports. This model has been extended to determine the grain size distribution for various dopant concentration, annealing time and temperature. The results are discussed in detail.

Keywords : Grain growth, phosphorus doped polysilicon, kinetic model

PACS Nos. : 81.05.Bx, 81.15.Aa

1. Introduction

Grain boundary migration is the movement of a grain boundary perpendicular to the tangent plane. Therefore, boundary migration is based, in principle, always on the movement of atoms from one grain to the other across the grain boundary [1]. All the reported theories [2–5] for grain boundary migration assigned that the elementary process of grain boundary migration is the diffusion of one atom or a group of atoms from the surface of the shrinking grain to the surface of the growing grain. The different atomic jumps were considered to be independent of each other. Therefore, the calculated migration rate is independent of the boundary orientation relative to the grains. Lucke and coworkers [6,7] have reported the 0.01% of manganese or iron can decrease the rate of recrystallization of high purity aluminium by factors 10^{12} or 10^{16} respectively.

Polycrystalline silicon layers are extensively used in the fabrication of high value resistors, gate electrodes and diffusion source for both active devices as well as contacts. Thermal treatment has been applied for the fabrication of devices. Many experimental results [8–14] on grain growth in polycrystalline silicon are available. But concerning the grain growth mechanism, only a few results [15–21] have been reported. Therefore, understanding the role of impurities is absolutely essential for the understanding of grain growth mechanism in polycrystalline silicon. In the present communication, we have proposed a kinetic model to determine the grain surface area in heavily phosphorus doped polycrystalline silicon for different phosphorus concentration, annealing time and temperature.

Many theoretical reports on grain size distribution are available [22–29], but there are no reports on the grain size distribution in heavily phosphorus doped polysilicon during high temperature annealing except the work [18]. In the second part of this paper, the model has been extended to determine the grain size distribution in heavily phosphorus doped polysilicon for different dopant concentration, annealing time and annealing temperature based on the thermodynamical concepts. The results are discussed in detail.

2. Theory

The driving force leading to grain boundary migration in polycrystalline materials is determined not only by the physical constants characteristic of the substances in bulk, but also by the shapes and dimensions of the individual grains. The driving force for the movement of atoms from one grain to another [17] is

$$E = \frac{Sa^3G}{r}, \quad (1)$$

where S is a constant related to the geometric shape of the grains (for spherical grains $S = 2$), G is the grain boundary energy, a is the lattice constant and r is the grain size.

From basic rate theory, the net rate of atomic transfer across the boundary (from lattice sites of one grain to those of a neighbour) is given by [11]

$$I = D_g/J^2 [1 - \exp(-E/kT)], \quad (2)$$

where D_g is the self diffusion constant of the atoms across the grain boundary, k is the Boltzmann's constant, T is the annealing temperature and J is the thickness of the grain boundary. The rate of boundary motion is given by the product of the net rate of atomic transfer across the boundary and the thickness. Therefore, the grain growth rate becomes

$$dr/dt = IJ = D_g/J [1 - \exp(-E/kT)]. \quad (3)$$

Expanding the exponential, neglecting the cube and higher powers of E/kT and using eq. (1)

$$dr/dt = 2D_g/J (L/r) [1 - (L/r)], \quad (4)$$

where $L = Sa^3G/2kT$

Integrating eq. (4)

$$2D_g Lt/J = \left[(r-L)^2/2 + 2L(r-L) + L^2 \log(r-L) \right]_{r_0}^r, \quad (5)$$

where t is the time of annealing and r_0 is the initial grain size. Expanding the right hand side of eq. (5), applying the limits and neglecting the cube and higher power of L , since L is very small compared to the value of r_0 , we get

$$r = (r_0 + L) \left[\left(1 + 4D_g Lt/J(r_0 + L)^2 \right)^{1/2} - L/(r_0 + L) \right]. \quad (6)$$

It is noted that the grain size is directly proportional to the square root of annealing time as reported earlier [9,11,17-20]. Surface area of the grain can be written from eq. (6) as

$$A = A_0 \left[1 + 4D_g Lt/Jr_0(L + r_0) \right], \quad (7)$$

where A_0 is the initial grain surface area. It is apparent from eq. (7), that the grain surface area increase linearly with the annealing time. The rate at which the grain surface area increases with time is the strong fraction of annealing temperature and dopant concentration through the relation

$$\text{Slope} = 4A_0 D_g L/Jr_0(L + r_0).$$

This model has been extended to evaluate the grain size distribution for different annealing time and annealing temperatures. By introducing a dimensionless variable known as relative grain size, $R = r/r_{cr}$ in eq. (4) and simplifying

$$dR^2/d\tau = \left[(4D_g L/J) (1 - L/Rr_{cr}) dt/dr_{cr} - R^2 \right], \quad (8)$$

where $1/d\tau = r^2/dr_{cr}$, r_{cr} is the average grain size and $d\tau$ represents the time.

$$dR^2/d\tau = [X(1 - Y/R) - R^2],$$

where $X = (4D_g L/J) dt/dr_{cr}$ and $Y = L/r_{cr}$

$$dR/d\tau = 1/2R [X(1 - Y/R) - R^2] \quad (9)$$

The eq. (9) has been solved to determine the steady state grain size distribution during normal grain growth mechanism, using the Hillert's [23] approach. The whole distribution of the individual grain size during the steady state is given by

$$P(R) = 1/2 \exp(-\beta\psi/2)/[dR/d\tau], \quad (10)$$

where β is a constant ($\beta = 2$ for two dimensional system $\beta = 3$ for three dimensional system) and

$$\psi = \int_R^R dR/(-dR/d\tau) \quad (11)$$

From eqs. (9) and (11)

$$\psi = \left[\frac{2}{3} \log(R^3 - XR + XY) \right]_0^R + W, \quad (12)$$

where
$$W = \frac{2X}{3} \int_0^R \frac{dR}{(R^3 - XR + XY)}.$$

Numerical integration method has been followed to determine the value of W . Substituting the values of $dR/d\tau$ and ψ from eqs. (9) and (12) respectively in eq. (10), we get

$$P(R) = \beta R^2 / (R^3 - XR + XY) \left[(R^3 - XR + XY) / XY \right]^{\beta/3} \exp(\beta W/2). \quad (13)$$

Eq. (13) gives the grain size distribution.

3. Computer programme

Computer simulation technique has been used to evaluate the numerical values of our theoretical findings. The grain boundary self diffusion of silicon atoms is function of impurity concentration in the grain and the grain boundary free energy changes with the grain size. To determine the grain boundary self diffusion of silicon atoms, we have followed the method used by Mei *et al* [9] to our expression for the grain size eq. (6) and grain surface area given by eq. (7). The grain boundary self diffusion has been determined for each time step of one minute from the size of previous time step for the given doping concentration and annealing temperature. Using the grain boundary self diffusion values, the size of the grain at a given time step is evaluated. This process is reiterated until the time of annealing is achieved. The size distribution of grain in the polysilicon at the end of each time is also evaluated using eq. (13).

4. Results and discussion

The simulated numerical values of the radius of the grain and surface area of the grain in polysilicon doped with phosphorus have been presented in Figures (1–6). Figure 1 depicts the radius and surface area of the grain vs annealing temperature for four different dopant concentrations and for one hour of annealing time. It is observed that the radius and surface area of the grain increases with dopant concentration and annealing temperature. The reported experimental values [9] for the radius of the grain have also been plotted in the graph. The activation energy for the grain growth is found to be 0.5028 eV. Figure 2 is drawn for the radius and surface area of the grain as a function of annealing temperature for different annealing times and for phosphorus concentration of 6×10^{25} atoms/m³. It is observed from the graph that the grain increases with annealing time for all the annealing temperatures. Figure 3 shows variation of the radius and surface area of the grain with the dopant concentration for various annealing time and for the given annealing temperature of

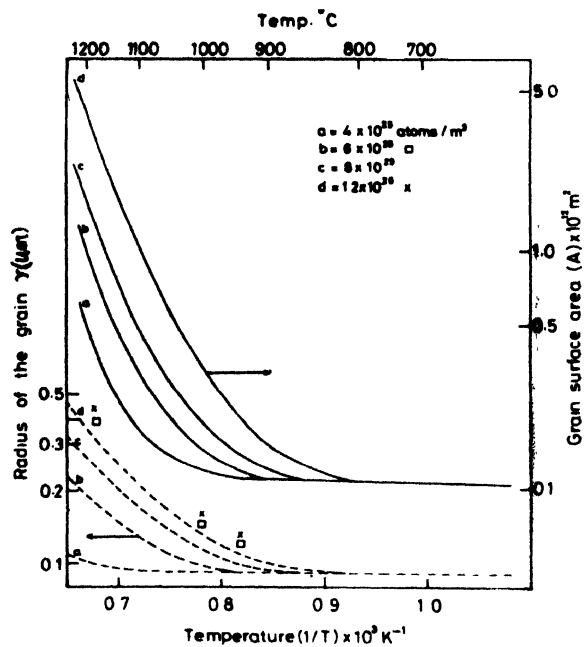


Figure 1. Radius of grain (— —) and surface area of the grain (—) vs annealing temperature of P doped polysilicon after one hour annealing for various values of dopant concentration. Available experimental reports [8] are given (X, □).

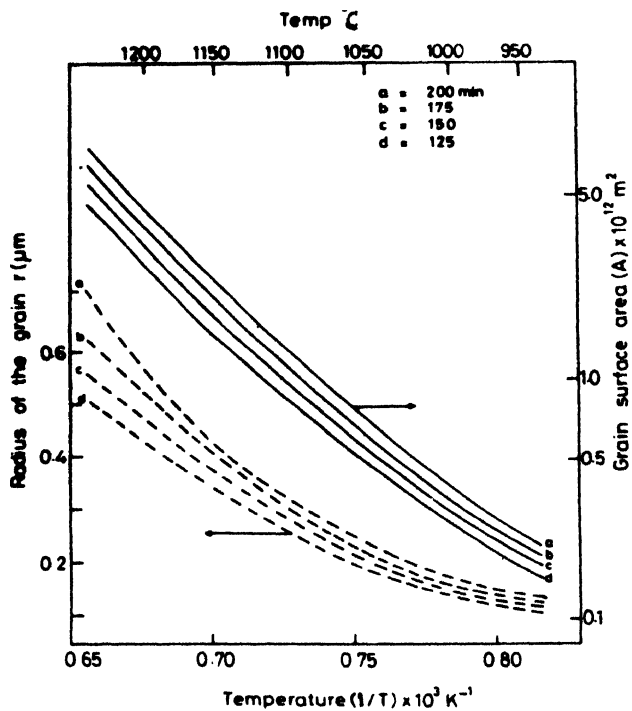
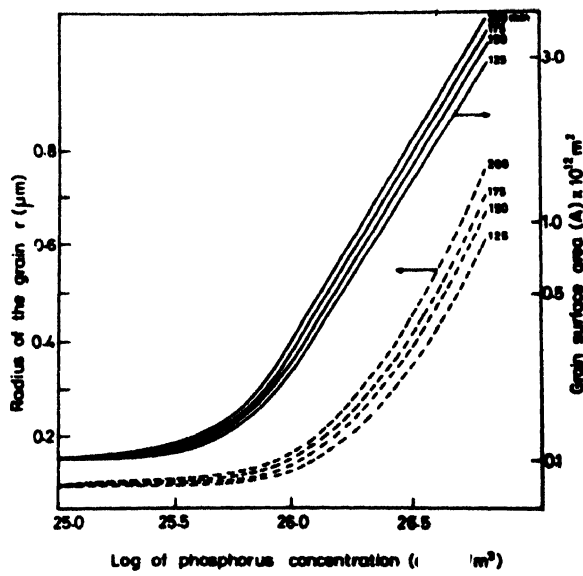


Figure 2. Variation of radius and surface area as a function of annealing temperature for various annealing times and for P concentration of 6×10^{25} atoms/m³.

1173 K. Figure 4 depicts the radius and surface area of the grain vs dopant concentration for different annealing temperature. It reveals that the grain size and surface area of the grain

increases with annealing temperature. The reported experimental values [9] for radius of the grain have also been plotted in the graph. Figure 5 shows the radius of the grain and surface



area vs annealing time for different phosphorus concentration and for one hour of annealing and annealing temperature of 1273 K.

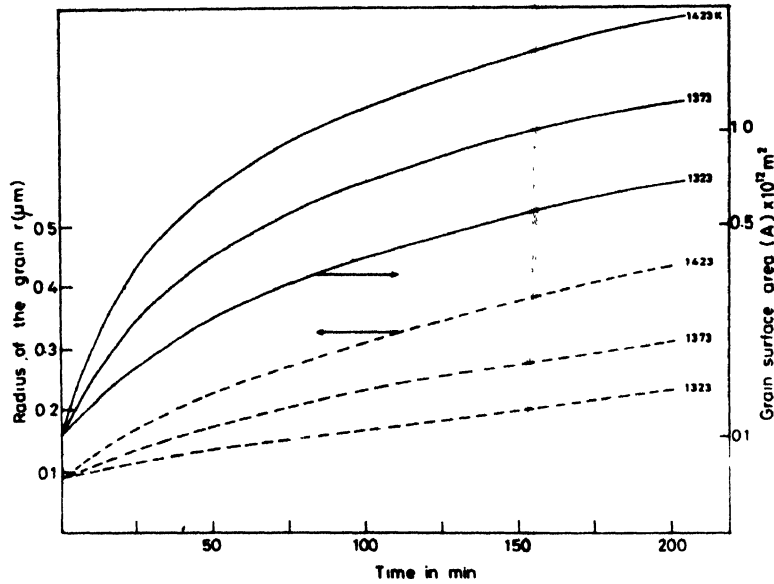


Figure 5. Variation of radius of the grain and surface area with annealing time for different annealing temperatures and for P concentration of 6×10^{25} atoms/m³.

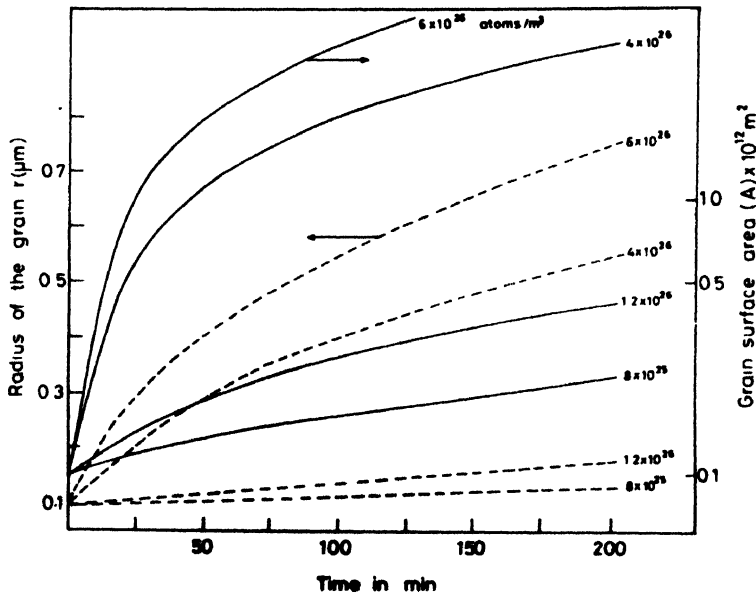


Figure 6. Radius of the grain and surface area vs annealing time for different P concentrations and annealing temperature 1273 K.

Figure 7 shows the grain size distribution vs relative grain size for the annealing temperature of 1273 K, for the phosphorus concentration of 8×10^{25} atoms/m³ and

for three different annealing times. From the graph it is concluded that the grain size distribution increases with annealing time. Dotted line in the Figure 7 shows the

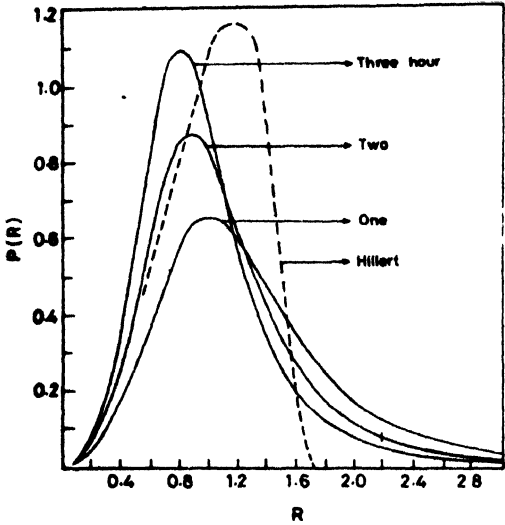


Figure 7. Variation of grain size distribution vs relative grain size for annealing temperature of 1273 K and P concentration 8×10^{25} atoms/ m^3 , for three different annealing times. Dotted line shows Hillert's report.

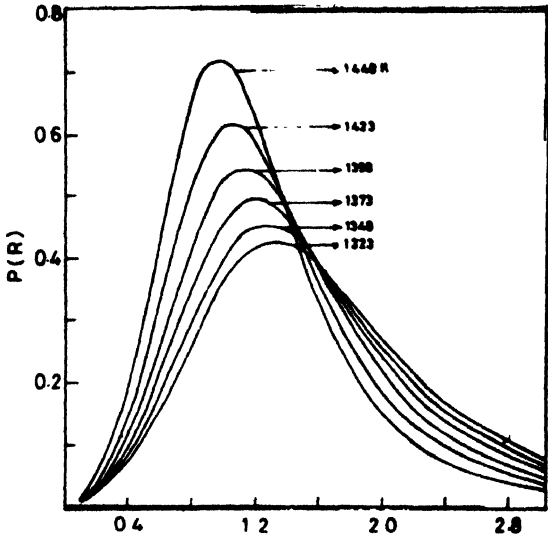


Figure 8. Grain size distribution vs relative grain size for one hour of annealing and P concentration of 6×10^{25} atoms/ m^3 for six different annealing temperatures.

Hillert's [23] result. Figure 8 depicts grain size distribution vs relative grain size for one hour of annealing and phosphorus concentration of 6×10^{25} atoms/ m^3 and for six different

annealing temperatures. It is observed that the grain size distribution increases with annealing temperature. Figure 9 is plotted between the grain size distribution and relative

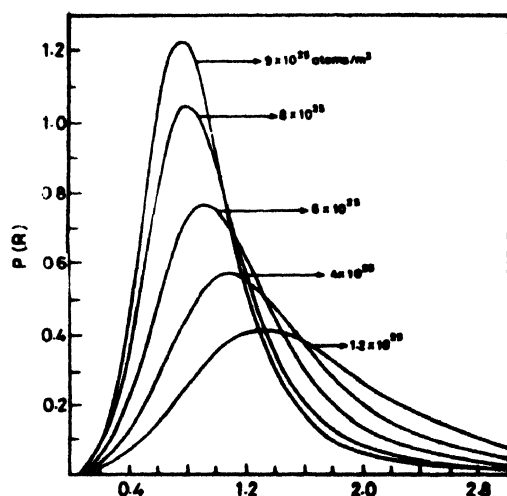


Figure 9. Grain size distribution vs relative grain size for two hours of annealing and annealing temperature 1273 K for five different P concentrations.

grain size for two hours of annealing and annealing temperature of 1273 K for five different phosphorus concentrations. From the Figures (7–9) it is observed that the grain size distribution slowly increases with increase of relative grain size, and attains a maximum, then with further increase of relative grain size, the grain size distribution decreases. The grain size distribution increases with phosphorus concentration, annealing time and annealing temperature. The reason attributed is due to the large amount of phosphorus atoms segregated at the grain boundaries.

5. Conclusions

We have concluded that the radius, surface area and size distribution of the grain in polysilicon doped with phosphorus increases with dopant concentration, annealing time and annealing temperature.

Acknowledgment

The authors thank the University Grants Commission, New Delhi and Rev Fr P Soundararaju, S D B, Principal, Sacred Heart College, Tirupattur, Vellore District for the financial assistance. One of the authors (S Kalainathan) is very grateful to the Chairman, the Vice-Chairman and the Principal of Vellore Engineering College for their constant encouragement to carry out this research work.

References

- [1] H Gleiter *Acta. Metal.* 17 853 (1969)
- [2] N F Mott *Proc. Roy. Soc. (London)* 60 391 (1948)
- [3] D Turnbull *Trans. Am. Inst. Min. Metal. Engrs.* 191 3 (1954)
- [4] D G Cole, P Feltham and E Gilliam *Proc. Roy. Soc. (London)* 67 131 (1954)

- [5] H Miura, N Saito and N Okamoto *Microelectronics J.* **26** 249 (1995)
- [6] K Lucke, G Masing and P Noiting *Z. Metal.* **15** 64 (1956)
- [7] K Detert and K Lucke *Influence of Defined Small Amounts Impurities on the Recrystallization of Aluminium* (Brown University Report) # AFOS R-TN-56-103; AD-82016-March (1956)
- [8] Y Wada and S Nishimatsu *J. Electrochem. Soc.* **125** 1499 (1978)
- [9] L Mei, M Rivier, Y Kwart and R W Dutton *J. Electrochem. Soc.* **129** 1791 (1982)
- [10] R Angelucci, M Severi and S Solmi *Mater. Chem. Res. Soc. Proc.* **23** 627 (1984)
- [11] C V Thomson and H I Smith *J. Appl. Phys. Lett.* **44** 603 (1984)
- [12] Ishihara and M Matsumura *Electronics Lett.* **31** 1956 (1995)
- [13] Kuriyama, T Nohda, Y Aya, T Kuwahara, S Kiyama and S Tsuda *Japanese J. Appl. Phys.* **33** 5657 (1994)
- [14] Mei, J B Boyce, M Hack, R Lujan, S E Ready, D E Fork, R I Johnson, G B Anderson *J. Appl. Phys.* **76** 3194 (1994)
- [15] L Mei, M River, Y Kwart and R W Dutton *Semiconductor Silicon* eds. H R Juff, R J Keregler and Y Takeishu p 1007 (1981)
- [16] C V Thomson *J. Appl. Phys.* **58** 763 (1985)
- [17] S Kalainathan, R Dhanasekaran and P Ramasamy *Thin Solid Films* **163** 383 (1988)
- [18] S Kalainathan, R Dhanasekaran and P Ramasamy *J. Crystal Growth* **104** 383 (1990)
- [19] S Kalainathan, R Dhanasekaran and P Ramasamy *J. Electron. Mater.* **19** 250 (1990)
- [20] S Kalainathan, R Dhanasekaran and P Ramasamy *J. Mater. Sci. - Materials in Electronics* **2** 98 (1991)
- [21] F Jesu Rethinam, S Kalainathan and C Thirupathu *Indian J. Phys.* **72A** 117 (1998)
- [22] I M Lifshitz and V V Slezov *Soviet Phys. JETP* **35** 331 (1959)
- [23] M Hillert *Acta. Metal.* **13** 227 (1965)
- [24] O Hunderi and N Ryum *Acta. Metal.* **27** 161 (1979)
- [25] O Hunderi and N Ryum *J. Mater. Sci.* **15** 1104 (1980)
- [26] P S Sahni, D J Srolovitz, G S Grest, M P Anderson and S A Safran *Phys. Rev.* **B28** 2705 (1983)
- [27] S J Srolovitz, M P Anderson, G S Grest and P S Sahni *Scr. Metal.* **17** 241 (1983)
- [28] S J Srolovitz, M P Anderson, G S Grest and P S Sahni *Acta. Metal.* **32** 1429 (1984)
- [29] Garbacz, B Ralph and K J Kurzydowski *Acta. Metal. Mater.* **43** 1541 (1995)